## Refine Search

#### Search Results -

Terms	Documents
L40 and charg\$3	37

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Database:

L41			Refine Search
	Recall Text	or 1	Interrupt

### Search History

# DATE: Tuesday, December 21, 2004 Printable Copy Create Case

Set Name side by side	Query	<u>Hit</u> Count	<u>Set</u> <u>Name</u> result set
DB=B	PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<u>L41</u>	L40 and charg\$3	37	<u>L41</u>
<u>L40</u>	L39 and (current adj source)	37	<u>L40</u>
<u>L39</u>	L38 and (output adj voltage)	37	<u>L39</u>
<u>L38</u>	L37 and boost\$3	38	<u>L38</u>
<u>L37</u>	L36 and (constant adj current)	38	<u>L37</u>
<u>L36</u>	L35 and capacitor and resistor and diode	41	<u>L36</u>
<u>L35</u>	L34 and (reduc\$3 adj power)	45	<u>L35</u>
<u>L34</u>	(LED adj driver) and (voltage adj levels) and (forward near2 voltage)	84	<u>L34</u>
<u>L33</u>	L17 and (voltage adj doubler)	0	<u>L33</u>
<u>L32</u>	L21 and (voltage adj doubler)	0	<u>L32</u>
DB=U	USPT; PLUR=YES; OP=OR		
<u>L31</u>	6362578.pn.	1	<u>L31</u>
<u>L30</u>	6362578.pn.	1	<u>L30</u>

WEST Refine Search	Page 2 of 2
<u>L29</u> 6061218.pn DB=PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=	1 <u>L29</u> YES; OP=OR
<u>L28</u> L27 and boost\$3	3 <u>L28</u>
<u>L27</u> (LED adj backlight\$3) and (power adj levels)	17 <u>L27</u>
L26 (LED adj backlight\$3) and (power adj supply adj levels	s) 0 L26

(LED adj backlighting) and (power adj supply adj levels)

LCD and (light adj source) and controller and (constant adj current and

boost\$3 and (constant adj current) and (current adj source) and (LED adj

LED and (forward adj voltage) and (power adj supply) and driving

(power adj supply adj level) and boost\$3 and (constant adj current)

#### **END OF SEARCH HISTORY**

L25

L24

L23

L22

L21

L20

L19

L18

L17

L16

L15

L14

L13

L12

L11

L10

L9

L8

L7

L6

L5

L4

<u>L3</u>

<u>L2</u>

L1

L22 and bias\$3

L22 and (forward adj voltage)

L19 and LED and LCD

L18 and charg\$3

L14 and bias\$3

driver)

L13 and buffer\$3

L21 and (power adj consumption)

L14 and display and backlighting

L12 and transistor and diode and resistor

L7 and capacitor and diode and resistor

(power adj supply adj level) and LED and LCD

L2 and compensat\$3 and (current adj source)

L1 and capacitor and charg\$3 and discharg\$3

L15 and level and illumination

L15 and (level adj lower)

L10 and (LED adj driver)

L1 and LED and LCD

L2 and LED and LCD

L3 and LED and LCD

L9 and (backlit or backlight\$2)

source) and diode and capacitor and resistor

Ł -

0

0

23

71

4

28

28

1

0

37

50

51

10

92

12

37

0

0

0

11

20

56

548

L25

L23

L22

L21

<u>L20</u>

L19

L18

L17

L16

L14

L13

L12

L11

L10

L9

<u>L8</u>

<u>L7</u>

<u>L6</u>

<u>L5</u>

<u>L4</u>

<u>L3</u>

<u>L2</u>

L1

37 L15

13 L24

# Refine Search

#### Search Results -

Terms	Documents
L39 and (constant adj current) and (current adj source)	8

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

Database:

L40			Refine Search
	Recall Text 🔷	Clear	Interrupt

## Search History

## DATE: Tuesday, December 21, 2004 Printable Copy Create Case

<u>Set</u>		Hit	<u>Set</u>
<u>Name</u>	Query	Count	<u>Name</u>
side by		Count	result
side			set
DB=	PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=OR		
<u>L40</u>	L39 and (constant adj current) and (current adj source)	8	<u>L40</u>
<u>L39</u>	LED and LCD and (display adj controller) and (power adj supply)	511	<u>L39</u>
<u>L38</u>	L37 and (output adj voltage)	12	<u>L38</u>
<u>L37</u>	(power adj transistor) and (light adj emitting adj diode) and (constant adj current) and (current adj source) and boost\$3	23	<u>L37</u>
<u>L36</u>	(power adj supply adj level) and (forward adj voltage) and (voltage adj drop) and LED	4	<u>L36</u>
<u>L35</u>	L33 and boost\$3	1	<u>L35</u>
<u>L34</u>	L33 and (low adj voltage) and boost\$3	0	<u>L34</u>
<u>L33</u>	(driving adj light adj emitting adj diode) and (constant adj current) and (current adj source)	47	<u>L33</u>
<u>L32</u>	(driving adj light adj emitting adj diode) and (constant adj current) and (current adj cource)	0	<u>L32</u>

<u>L31</u>	(driving adj light adj emitting adj diode) and boost\$3 and (constant adj current) and (current adj cource)	0	<u>L31</u>
<u>L30</u>	(driving adj light adj emitting adj diode) and (low adj voltage) and boost\$3 and (constant adj current) and (current adj cource)	0	<u>L30</u>
<u>L29</u>	L28 and driver	50	<u>L29</u>
<u>L28</u>	L27 and capacitor and buffer\$3	53	<u>L28</u>
<u>L27</u>	L26 and activat\$3	66	<u>L27</u>
<u>L26</u>	L25 and (output adj voltage) and transistor	99	<u>L26</u>
<u>L25</u>	(DC\$DC adj converter) and LED and (constant adj current) and (current adj source)	149	<u>L25</u>
<u>L24</u>	L23 and (output adj voltage)	28	<u>L24</u>
<u>L23</u>	L22 and boost\$3	29	L23
<u>L22</u>	(constant adj current) and (current adj source)and (LED adj controller)	45	L22
<u>L21</u>	116 and (LED adj controller)	0	<u>L21</u>
<u>L20</u>	L19 and (power adj supply)	20	L20
<u>L19</u>	L18 and resistor and diode	22	<u>L19</u>
<u>L18</u>	L17 and capacitor	22	<u>L18</u>
<u>L17</u>	L16 and (driver adj circuit) and boost\$3	22	L17
<u>L16</u>	L15 and (current adj source) and (constant adj current)	307	<u>L16</u>
<u>L15</u>	LED and (voltage adj drop) and forward and bias\$3 and transistor and collector and emitter and current	1616	<u>L15</u>
<u>L14</u>	LED and (voltage adj drop) and forward and bias\$3 and transistor collector and emitter and current	114206	<u>L14</u>
<u>L13</u>	L11 and (backlit or backlight\$3)	28	<u>L13</u>
<u>L12</u>	L11 and illumination	2	<u>L12</u>
<u>L11</u>	L10 and charg\$3 and discharg\$3	49	<u>L11</u>
<u>L10</u>	L9 and LED	52	<u>L10</u>
<u>L9</u>	L8 and (current adj source)	99	<u>L9</u>
<u>L8</u>	L7 and (constant adj current)	119	<u>L8</u>
<u>L7</u>	(driver adj circuit) and (voltage adj drop) and boost\$3 and forward and bias\$3 and transistor and capacitor and diode and resistor	363	<u>L7</u>
<u>L6</u>	L5 and (voltage adj drop)	9	<u>L6</u>
<u>L5</u>	L4 and bias\$3 and forward	11	<u>L5</u>
<u>L4</u>	L3 and LCD	12	<u>L4</u>
<u>L3</u>	driver and LED and transistor and boost\$3 and converter and compensat\$3 and (power near3 level) and charg\$3 and discharg\$3 and capacitor and diode and resistor and (current adj limit\$3)	97	<u>L3</u>
<u>L2</u>	driver and LED and transistor and boost\$3 and converter and compensat\$3 and (inadequate near3 level) and charg\$3 and discharg\$3 and capacitor and diode and resistor and (current adj limit\$3)	3	<u>L2</u>
<u>L1</u>	driver and LED and transistor and boost\$3 and converter and compensat\$3 and (inadequate near3 level) abd charg\$3 and discharg\$3 and capacitor and diode and resistor and (current adj limit\$3)	22446	<u>L1</u>